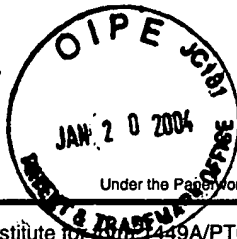


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Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

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Sheet 1 of 7

Complete if Known

Application / Conf. No.	10/086,129 / 2232
Filing Date	February 28, 2002
First Named Inventor	David P. Schultz
Art Unit	2133
Examiner Name	Unknown
Attorney Docket Number	X-1069 US

U.S. PATENT DOCUMENTS

CITED PATENT DOCUMENTS					
Examiner Initials *	Cite No. ¹	Document Number Number - Kind Code (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
[Signature]		US- 4,758,985	07-19-88	Carter	RECEIVED JAN 22 2004 Technology Center 2100
		US- 5,142,625	08-25-92	Nakai	
		US- 4,855,669	08-08-89	Mahoney	
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		US- 5,072,418	12-10-91	Boutaud et al.	
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[Signature]		EP 0315275 A2	10-05-89	LSI Logic		
		WO 93 25968 A1	12-23-93	Furtek		
		EP 0 905 906 A2	03-31-99	Lucent		
		EP 1 235 351 A1	08-28-02	Matsushita Electric		

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Sheet 2 of 7

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Attorney Docket Number	X-1069 US

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OTHER -- NON PATENT LITERATURE DOCUMENTS

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<i>[Signature]</i>		SAYFE KIAEI et al., "VLSI DESIGN OF DYNAMICALLY RECONFIGURABLE ARRAY PROCESSOR-DRAP," IEEE, February 1989, pp. 2484-2488, V3.6, IEEE, 3 Park Avenue, 17th Floor, New York, NY 10016-5997	
<i>[Signature]</i>		VASON P. SRINL, "FIELD PROGRAMMABLE GATE ARRAY (FPGA) IMPLEMENTATION OF DIGITAL SYSTEMS: AN ALTERNATIVE TO ASIC," IEEE, May 1991, pp. 309-314, IEEE, 3 Park Avenue, 17th Floor, New York, NY 10016-5997	
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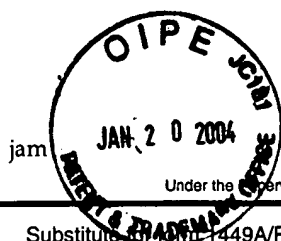
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Application / Conf. No.	10/086,129 / 2232
Filing Date	February 28, 2002
First Named Inventor	David P. Schultz
Art Unit	2133
Examiner Name	Unknown
Attorney Docket Number	X-1069 US

U.S. PATENT DOCUMENTS

Examiner Initials *	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code (if known)			
		US- 5,473,267	12-05-95	Stansfield	
		US- 5,742,180	04-21-98	DeHon et al.	
		US- 5,737,631	04-07-98	Trimberger	
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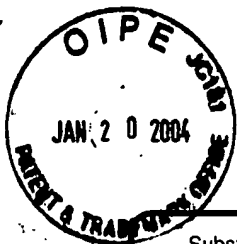
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Application / Conf. No.	10/086,129 / 2232
Filing Date	February 28, 2002
First Named Inventor	David P. Schultz
Art Unit	2133
Examiner Name	Unknown
Attorney Docket Number	X-1069 US

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		XILINX, INC., "THE PROGRAMMABLE LOGIC DATA BOOK," 1994, Revised 1995, Xilinx, Inc., 2100 Logic Drive, San Jose, CA. 95124.	
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Sheet 5 of 7

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Application / Conf. No.	10/086,129 / 2232
Filing Date	February 28, 2002
First Named Inventor	David P. Schultz
Art Unit	2133
Examiner Name	Unknown
Attorney Docket Number	X-1069 US

U.S. PATENT DOCUMENTS

Examiner Initials *	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code (if known)			
		US- 6,011,407	01-04-00	New	
		US- 6,172,990	01-09-01	Deb et al.	
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		US- 09/991,412	11-16-01	Herron et al.	
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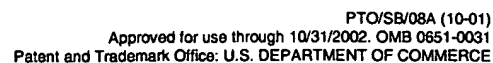
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Attorney Docket Number	X-1069 US

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Sheet	6	of	7	Attorney Docket Number	X-1069 US
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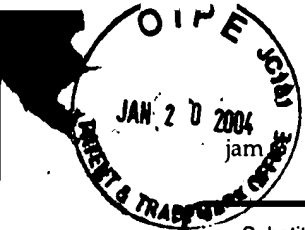
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		US- 6,434,735	08-13-02	Watkins	
		US- 6,588,006	07-01-03	Watkins	
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		US- 09/917,304	07-27-01	Douglass et al.	
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Filing Date	February 28, 2002
First Named Inventor	David P. Schultz
Art Unit	2133
Examiner Name	Unknown
Attorney Docket Number	X-1069 US

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